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Substitute for form 1449B/PTO				Complete if Known	
INFORMATION DISCLOSURE STATEMENT BY APPLICANT				Application Number	10/620,194
				Filing Date	07/15/2003
				First Named Inventor	Eric N. Paton
				Group Art Unit	2823
				Examiner Name	Joanne A. Garcia
(use as many sheets as necessary)				Attorney Docket Number	039153-0646 (H0975)
Sheet	1	of	2		

U.S. PATENT DOCUMENTS

FOREIGN PATENT DOCUMENTS

Examiner Initials ^a	Cite No. ¹	Foreign Patent Document			Name of Patentee or Applicant of Cited Documents	Date of Publication of Cited Document MM-DD-YYYY	Pages, Columns, Lines, Where Relevant Passages or Relevant Figures Appear	T ⁶
		Office ³	Number ⁴	Kind Code ^b (if known)				
JAG	A15	WO	02/101818	A2	Currie et al.	12/19/2002		
JAG	A16	WO	02/095818	A1	Beyer et al.	11/28/2002		
JAG	A17	GB	2 254 731	A	Samsung Electronics Company Limited	10-14-1992		
JAG	A18	EP	0 245 622	B1	International Business Machines Corporation	11-19-1987		

NON PATENT LITERATURE DOCUMENTS

Examiner Initials* 	Cite No. ¹ A19	Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.) date, page(s), volume-issue number(s), publisher, city and/or country where published.
		Written Opinion of the International Searching Authority; International Application No. PCT/US2004/007464; International Filing Date 03-11-2004; 6 pgs.

Examiner Signature		Date Considered	April 13 2005
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*EXAMINER: Initial if reference considered, whether or not citation is in conformance with MPEP 609. Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.

¹ Unique citation designation number. ² See Attached Kinds of U.S. Patent Documents. ³ Enter Office that issued the document, by the two-letter code (WIPO Standard ST.31). ⁴ For Japanese patent documents, the indication of the year of the reign of the Emperor must precede the serial number of the patent document.

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Substitute for form 1449B/PTO				Complete if Known	
INFORMATION DISCLOSURE STATEMENT BY APPLICANT				Application Number	10/620,194
Date Submitted: February 7, 2005 (use as many sheets as necessary)				Filing Date	07/15/2003
				First Named Inventor	Eric N. Paton
				Group Art Unit	2823
				Examiner Name	Joannie A. Garcia
Sheet	2	of	2	Attorney Docket Number	039153-0646 (H0975)

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Examiner Initials*	Cite No. ¹	Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.) date, page(s), volume-issue number(s), publisher, city and/or country where published.		T ²
JAG	A20	International Search Report; International Application No. PCT/US2004/007464; mailed 01/09/2004; 3 pgs.		
JAG	A21	OHKUBO, Satoshi; TAMURA Yasuyuki; SUGINO, Rinji; NAKANISHI, Toshiro; SUGITA, Yoshihiro; AWAJI, Naoki and TAKASAKI, Kanetake; "High Quality Ultra-Thin (4nm) Gate Oxide by UV/O ₃ Surface Pre-Treatment of Native Oxide"; 1995 Symposium On VLSI Technology; Digest of Technical Papers; June 6-8, 1995; Kyoto, Japan; 3 pgs.		
JAG	A22	Written Opinion of the International Searching Authority; International Application No. PCT/US2004/000982; International Filing Date 01-13-2004; 6 pgs.		
JAG	A23	International Search Report for PCT/US2004/000982, 4 pages.		
JAG	A24	Wolf, Stanley, Ph.D. and Tauber, Richard N., Ph.D.; "Silicon Processing For The VLSI Era"; Vol. 1: Process Technology; Lattice Press; 1986; pp. 57-58, 194		
JAG	A25	Ghandhi, Sorab K. "VLSI Fabrication Principles"; John Wiley and Sons; 1983; pp. 422-423		
JAG	A26	VAN ZANT, Peter, "Microchip Fabrication", McGraw Hill, 1977, pages 31 and 39		
JAG	A27	VOSSEN, John L. and KERN, Werner, "Thin Film Processes II", Academic Press, 1991, page 333		
JAG	A28	RIM, K., WELSER, J., HOYT, J.L., and GIBBONS, J.F., "Enhanced Hole Mobilities in Surface-channel Strained-Si <i>p</i> -MOSFETs", 1995 International Electron Devices Meeting TECHNICAL DIGEST, 5 pages.		
JAG	A29	WELSER, J., HOYT, J.L., TAKAGI, S. and GIBBONS, J.F., "Strain Dependence of the Performance Enhancement in Strained-Si <i>n</i> -MOSFETs", 1994 International Electron Devices Meeting TECHNICAL DIGEST, San Francisco, CA December 11-14, 1994, 5 pages.		

Examiner Signature	<i>Joannie A. Garcia</i>	Date Considered	<i>April 13 2005</i>
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